IEC60730BCM0L41RN

IEC60730B Library Release notes CM0 v4.1

Rev. 0 — 12/2020 Release Notes

by: NXP Semiconductors

Contents

1 Introduction

IEC60730B_CM0_4_1 is the actual version of the core self-test library for NXP devices with the CM0+ core. The library is certified by VDE. It is dedicated for use in applications compliant with the Safety class B standard (specified by IEC 60730, IEC60335 and/or UL 60730, and UL 1998).

The library is released in a precompiled format, together with functional example projects and documentation describing the respective tests.

The library is created in close cooperation with the application team, who have vast experience in customer projects. The customer feedback is also taken into consideration.

2 What is new

When compared to the previous version of the library, the main changes are:

- The WDOG function is consolidated - there is now only one function for one reference timer with parameters that enable you to select a correct WDOG refresh sequence.
- The following new devices are added: MK32L2A, MK32L2B, LPC51U68.

2.1 Description

The supported devices are as follows:

- MKV1x
- MKLxx
- MKE0x
- MKE1xZ
- K32L2Axx
- K32L2Bxx
- LPC51U68
- LPC84x
- LPC82x
- LPC80x

The supported/recommended IDEs are as follows:

- IAR v8.50 and higher
- Keil µVision V5.33(C compiler V6) and higher
- MCUXpresso IDE V11.3 and higher

The tested components are as follows:

- CPU registers
- Program counter
- Variable memory (RAM)
- Invariable memory (flash)
- Clock
- Digital I/O
- Analog I/O
- Stack
- Watchdog
- Touch Sensing Interface (TSI)

3 Optimizations, improvements, and changes

3.1 Library

The WDOG test functions are consolidated. The functions which use the same reference timer are merged into one with a parameter to select the refresh sequence.

The WDOG check function is consolidated.

The backup WDOG type *fs_wdog_test_t* is extended (the previous parts are in **bold**):

typedef struct {

uint32_t ***counter****;*

uint32_t ***resets****;*

uint32_t ***wdTestUncompleteFlag****;*

uint32_t RefTimerBase;

uint32_t WdogBase;

uint32_t pResetDetectRegister;

uint32_t ResetDetectMask;

} volatile fs_wdog_test_t;

- *RefTimerBase* - The base address of the reference timer
- *WdogBase* - The base address of the WDOG used
- *pResetDetectRegister* - The address of the reset-detect register
- *ResetDetectMask* - The mask for the WDOG reset flag

Table 1. Conversion table for WDOG function

New/edited function	Covered old function with new parameter
FS_WDOG_Setup_LPTMR()	• FS_WDOG_Setup() - Parameters "FS_KINETIS_WDOG" • FS_WDOG_Setup_COP() - Parameters "FS_COP_WDOG"

Table continues on the next page...

Table 1. Conversion table for WDOG function (continued)

New/edited function	Covered old function with new parameter
	• FS_WDOG_Setup_KE1XZ() - Parameters "FS_WDOG32" • FS_WDOG_Setup_KE1XF() - Parameters "FS_WDOG32"
FS_WDOG_Setup_IMX_GPT()	• FS_WDOG_Setup_RT() - Parameters "FS_IMXRT" • Also suitable for I.MX8mx - Parameters "FS_IMX8M"
FS_WDOG_Check()	The fuction has the following new parameters: • *"clear_flag"* - clears the flag of the WDOG reset • *RegWide8b* - if 1 SRS register is read/write as 8b, otherwise as 32b

3.2 Documentation

The documents for all test routines are merged into one document.

3.3 Examples

The example projects are available only in the MCUXpresso SDK as middleware.

To open an example, use the link from the table at http://www.nxp.com/iec60730 or perform the following steps:

- Go to http://mcuxpresso.nxp.com.
- Click Select Development Board.
- Select the supported board and click to add the Safety middleware.
- Build and download the SDK package.

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